

**VIRTUAL FACTORY: FACTORY LAYOUT DESIGN BASED ON SYSTEMATIC
LAYOUT PLANNING (SLP) FOR ELECTRONIC FABRICATION
EXAMPLE: MOTHERBOARD FABRICATION**

By

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STATEMENT BY THE AUTHOR

I hereby declare that this submission is my own work and to the best of my knowledge, it contains no material previously published or written by another person, nor material which to a substantial extent has been accepted for the award of any other degree or diploma at any educational institution, except where due acknowledgement is made in the thesis.

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ABSTRACT

VIRTUAL FACTORY: FACTORY LAYOUT DESIGN BASED ON SYSTEMATIC LAYOUT PLANNING (SLP) FOR ELECTRONIC FABRICATION EXAMPLE: MOTHERBOARD FABRICATION

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Systematic Layout Planning method for factory layout has been learned several times for thesis program. Application of this method is basically centered to manufacturing factory for metal fabrication. Electronics fabrication is made as an option to learn the differentiation of pattern on designing factory layout. Discrete products between metal and electronics fabrication is approximately the same. Especially for electronic fabrication, almost all the procedures are executed in gradually way and the velocity of parts placement and handling is quick. Procedures of electronic fabrication has to be defined and basically to be described as a display. The output will provide a change to the application of production time and cost. Optimal design of the factory layout will be implemented and the best solution needs to be delivered. Discrete Event Simulation application needs to be used to confirm the time validity and possible bottleneck that may happen.

Keywords: systematic layout planning, factory layout, electronic fabrication, discrete event simulation.



DEDICATION

I dedicate this works to my lovely family and friends, for their support, constant helping hands, and companions.



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