

## GLOSSARY

ADC	: also known as an ADC or an A/D converter, it is an electronic circuit that measures a real-world signal and converts it to a digital representation of the signal.
DMA	: a capability provided by some computer bus architectures that enables data to be sent directly from an attached device, such as a disk drive, to the main memory on the computer's motherboard.
EMI	: unwanted noise or interference in an electrical path or circuit caused by an outside source.
HVDC	: a bulk power transfer technology using direct current for transmission of electricity, in contrast to HVAC power systems which operate on alternating current.
IPC	: formally called the institute for printed circuits, is a global association for the electronic manufacturing industry.
I2C	: a standard bidirectional interface that uses a controller, known as the master, to communicate with slave devices.
SPI	: is an interface bus commonly used to send data between microcontrollers and small peripherals such as shift registers, sensors, and SD cards.
UART	: universal asynchronous receiver/transmitter and defines a protocol, or set of rules, for exchanging serial data between two devices.

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## APPENDIX

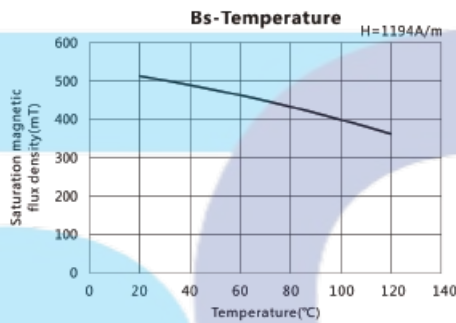
锰锌材料特性表与特性曲线 Material Characteristic Sheets and figures of MnZn



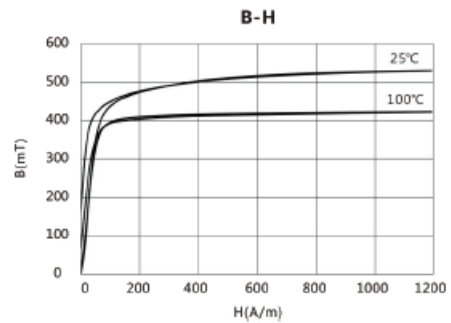
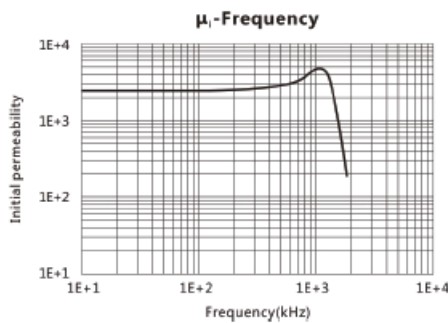
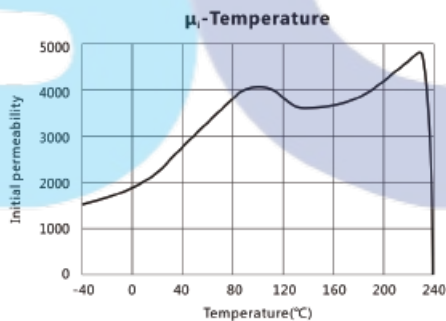
### 材料 / Material: TP4A

#### 特点 / Features:

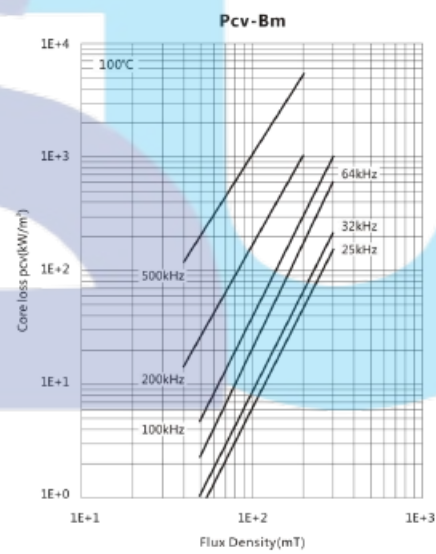
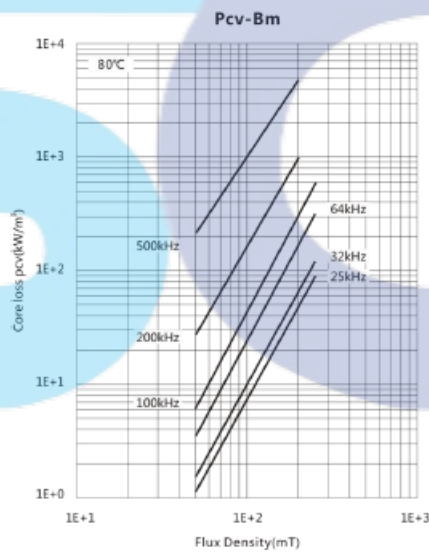
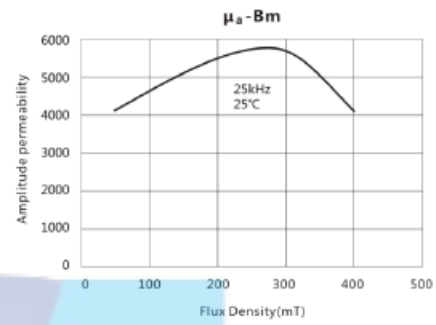
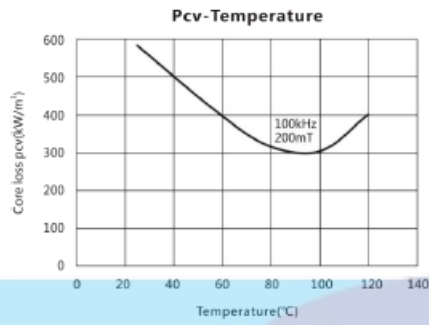
1. 主要应用于中频段(小于300kHz) / Mostly Used at Middle Frequency(Less than 300kHz)
2. 低磁心损耗, 高饱和磁感应强度 / Low Core Loss and High Saturation Flux Density
3. 损耗最低的温度点约在90°C / The Temperature Point of the Lowest Core Loss is 90°C



Initial permeability	$\mu_i$	25°C	2400±25%
Saturation magnetic flux density	Bs(mT)	25°C	510
		100°C	390
Remanence	Br(mT)	25°C	110
		100°C	60
Coercivity	Hc(A/m)	25°C	13
		100°C	6.5
Core loss	Pcv(kW/m³)	25°C	600
		100°C	300
		100kHz 200mT	120°C
Curie temperature	Tc(°C)	≥215	
Electrical resistivity	$\rho(\Omega\cdot m)$	6.5	
Density	d(kg/m³)	4.8×10³	
Test core : Toroid(mm)			
OD : 25			
ID : 15			
H : 7.5			



材料 / Material: TP4A



锰锌氧铁材料规格尺寸 Specification and Dimensions of MnZn Ferrite Material

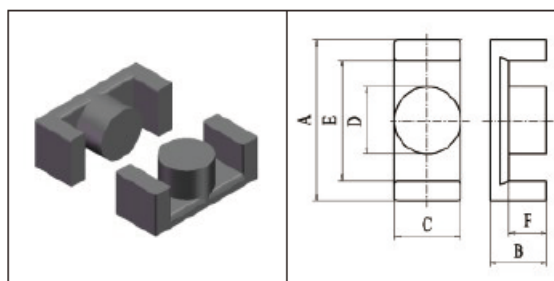


FIG.1

EER 型 / EER Core

型号 TYPE	图例 FIG.	尺寸 Dimensions (mm)					
		A	B	C	D	E	F
EER16/15/5	1	16.3±0.4	7.5±0.2	5.0±0.3	5.0+0.15/-0.3	11.5min	5.35±0.2
EER19.8/8.2/6.6B	1	19.8±0.4	4.1±0.15	6.6±0.2	6±0.15	15.8±0.3	1.9±0.15
EER25/12.4/11	2	25±0.35	6.2±0.2	11±0.2	9.5±0.2	19.5±0.35	2.95±0.2
EER25/18/11C	2	25.0±0.3	9.05±0.15	11.0±0.15	9.5±0.15	19.5±0.3	5.8±0.15
ETD25/27/10	2	25+0.5/-0.3	13.4+0.4/-0	9.5±0.25	9.5±0.25	18.9min	9.7-0.4/-0
EER25.5/18/8G	2	25.5±0.5	9.3±0.3	7.5±0.3	7.5±0.3	19.6min	6.2±0.3
EER26.8/26.6/8.5	2	26.8±0.4	13.3±0.15	8.5±0.2	8.4±0.2	20.4±0.4	10±0.2
EER28/28/11A	2	28.5±0.6	14±0.3	11.4±0.3	9.9±0.3	21.2min	9.6±0.3
EER28/34/11B	2	28.5±0.6	16.9±0.3	11.4±0.3	9.9±0.3	21.2min	12.5±0.3
EER28/17/11	2	28.55±0.55	8.35±0.2	11.4±0.25	9.9±0.25	21.2min	5.15±0.15
EER28/18/7.5	1	28±0.4	9±0.2	7.5±0.2	7.5±0.2	22.4±0.5	6.1±0.25
EER29/28/11A	2	29.2±0.8	14.4±0.3	11.3±0.3	9.9±0.3	21.8min	10.1±0.3
EER29.4/39.2/11.4	2	29.4±0.5	19.6±0.25	11.4±0.25	9.9±0.2	22.6min	15.3±0.2
EER29.5/30.6/11.4A	2	29.5±0.5	30.6±0.4	11.4±0.25	9.9±0.25	22.1±0.5	19.2+0.5/-0.4
ETD29/32/10B	2	29.8±0.8	15.8±0.3	9.5±0.3	9.5±0.3	22.0min	11.0±0.3
EER30/19/20B	2	30.0±0.5	9.4±0.15	20.3±0.3	13.2+0.3/-0.2	25.0min	6.6±0.2
EER30/28/11	2	30±0.55	14.3±0.25	11.4±0.25	9.9±0.2	23min	9.9±0.2
EER30/33/11	2	30±0.55	16.9±0.25	11.4±0.25	9.9±0.25	23min	12.53±0.28
EER33/34/14F	2	33±0.6	17.3±0.3	14±0.3	12.5±0.3	24.7min	12.8±0.3
EER34/35/11B	2	34.2±0.8	17.6±0.2	10.8±0.3	10.8±0.3	25.6min	12.4±0.3
ETD34/34/11B	2	34.2±0.8	17.3±0.2	10.8±0.3	10.8±0.3	25.6min	12.1±0.3

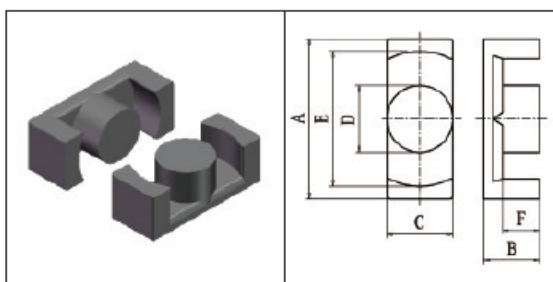


FIG. 2

**EER 型 / EER Core**

型号 TYPE	图例 FIG.	参数 Parameters				AL(Ref)*		重量(Ref) g / PRS
		C1	Ae	Le	Ve	TP4A	TPW33	
EER16/15/5	1	1.66	21.10	35.12	741.00	1200	1500	3.82
EER19.8/8.2/6.6B	1	0.85	28.96	24.73	716.2	2300	2600	3.8
EER25/12.4/11	2	0.45	71.60	32.35	2316.30	4400	5400	12.40
EER25/18/11C	2	0.61	71.62	43.75	3133.40	3450	4450	15.89
ETD25/27/10	2	0.91	67.27	60.90	4096.70	2410	3300	21.30
EER25.5/18/8G	2	1.05	44.93	47.38	2128.70	2050	2600	10.70
EER26.8/26.6/8.5	2	1.10	57.01	62.93	3587.80	1900	2450	18.20
EER28/28/11A	2	0.73	85.88	62.85	5397.80	2800	4000	28.43
EER28/34/11B	2	0.87	85.20	74.38	6336.90	2550	3600	32.93
EER28/17/11	2	0.55	78.63	43.45	3416.30	3800	5200	17.80
EER28/18/7.5	1	1.11	44.2	49.11	2170.7	2000	2400	12.11
EER29/28/11A	2	0.78	84.48	65.63	5544.40	2700	3600	28.86
EER29.4/39.2/11.4	2	1.05	82.72	86.73	7174.40	2150	2900	33.80
EER29.5/30.6/11.4A	2	0.72	90.15	64.55	5819.20	2850	3640	33.80
ETD29/32/10B	2	0.93	75.91	70.78	5373.10	2250	3150	27.80
EER30/19/20B	2	0.35	136.04	47.36	6442.20	6100	8350	34.20
EER30/28/11	2	0.78	84.83	65.79	5581.10	3100	3600	29.02
EER30/33/11	2	0.91	84.05	76.24	6408.10	3100	3500	33.90
EER33/34/14F	2	0.62	126.95	78.58	9975.40	3800	5100	50.32
EER34/35/11B	2	0.83	96.50	80.26	7744.60	2600	3900	40.00
ETD34/34/11B	2	0.82	96.57	79.06	7634.60	2500	3900	39.51

\*-AL为磁心对应参考值, 具体规格以双方签订承诺书为准; AL测试条件:1kHz 0.3V 10Ts; AL单位:nH/N2

\*-图例仅展示产品形状, 具体细节以实物或以双方签订承诺书为准。



**FR4 Data Sheet :-**

Test/Specification	FR4 Laminate Typical Values
Thermal Stress, Solder bath 288 deg. C	>60
Dimensional Stability, E-2/150	<0.04% Warp/fill
	<1.00% Bow/Twist
Flammability, Classification UL94	V0
Water Absorption E-1/105	0.10%
Peel Strength After Thermal Stress	11 lb./in After 10s/288 Deg. C
Flexural Strength	100,000 lbf/in <sup>2</sup> Lengthwise
	75,000 lbf/in <sup>2</sup> Crosswise
Resistivity After Damp Heat Volume	10 <sup>-8</sup> M ohms cm
Resistivity After Damp Heat Surface	10 <sup>-8</sup> M ohms
Dielectric Breakdown. Parallel to laminate	>60KV
Dielectric Constant @ 1MHz	4.7
Dissipation Factor @ 1MHz	0.014
Q-Resonance @ 1 MHz	>75
Q-Resonance @ 50 MHz	>95
Arc Resistance	125 s
Glass Transition Temperature	135 Deg. C
Temperature Index	130 Deg. C
<b>A Few Other Relevant Facts from other Sources</b>	
Specific Gravity	1.8-1.9
Rockwell Hardness (M scale)	110
Coefficient of Thermal Expansion	11 microns/m/Deg.C Lengthwise
	15 microns/m/Deg.C Crosswise
Thermal Conductivity	2.2-2.5 cal/h. cm Deg C





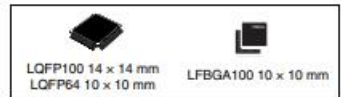
## STM32F105xx STM32F107xx

Connectivity line, ARM<sup>®</sup>-based 32-bit MCU with 64/256 KB Flash, USB  
OTG, Ethernet, 10 timers, 2 CANs, 2 ADCs, 14 communication interfaces

Datasheet - production data

### Features

- Core: ARM<sup>®</sup> 32-bit Cortex<sup>®</sup>-M3 CPU
  - 72 MHz maximum frequency, 1.25 DMIPS/MHz (Dhrystone 2.1) performance at 0 wait state memory access
  - Single-cycle multiplication and hardware division
- Memories
  - 64 to 256 Kbytes of Flash memory
  - 64 Kbytes of general-purpose SRAM
- Clock, reset and supply management
  - 2.0 to 3.6 V application supply and I/Os
  - POR, PDR, and programmable voltage detector (PVD)
  - 3-to-25 MHz crystal oscillator
  - Internal 8 MHz factory-trimmed RC
  - Internal 40 kHz RC with calibration
  - 32 kHz oscillator for RTC with calibration
- Low power
  - Sleep, Stop and Standby modes
  - VBAT supply for RTC and backup registers
- 2 × 12-bit, 1 μs A/D converters (16 channels)
  - Conversion range: 0 to 3.6 V
  - Sample and hold capability
  - Temperature sensor
  - up to 2 MSPS in interleaved mode
- 2 × 12-bit D/A converters
- DMA: 12-channel DMA controller
  - Supported peripherals: timers, ADCs, DAC, I2Ss, SPIs, I2Cs and USARTs
- Debug mode
  - Serial wire debug (SWD) & JTAG interfaces
  - Cortex<sup>®</sup>-M3 Embedded Trace Macrocell™
- Up to 80 fast I/O ports
  - 51/80 I/Os, all mappable on 16 external interrupt vectors and almost all 5 V-tolerant
- CRC calculation unit, 96-bit unique ID



- Up to 10 timers with pinout remap capability
  - Up to four 16-bit timers, each with up to 4 IC/OC/PWM or pulse counter and quadrature (incremental) encoder input
  - 1 × 16-bit motor control PWM timer with dead-time generation and emergency stop
  - 2 × watchdog timers (Independent and Window)
  - SysTick timer: a 24-bit downcounter
  - 2 × 16-bit basic timers to drive the DAC
- Up to 14 communication interfaces with pinout remap capability
  - Up to 2 × I2C interfaces (SMBus/PMBus)
  - Up to 5 USARTs (ISO 7816 interface, LIN, IrDA capability, modem control)
  - Up to 3 SPIs (18 Mbit/s), 2 with a multiplexed I2S interface that offers audio class accuracy via advanced PLL schemes
  - 2 × CAN interfaces (2.0B Active) with 512 bytes of dedicated SRAM
  - USB 2.0 full-speed device/host/OTG controller with on-chip PHY that supports HNP/SRP/ID with 1.25 Kbytes of dedicated SRAM
  - 10/100 Ethernet MAC with dedicated DMA and SRAM (4 Kbytes): IEEE 1588 hardware support, MII/RMII available on all packages

Table 1. Device summary

Reference	Part number
STM32F105xx	STM32F105RB, STM32F105VB
	STM32F105RB, STM32F105VB
	STM32F105RC, STM32F105VC
STM32F107xx	STM32F107RB, STM32F107VB
	STM32F107RC, STM32F107VC

March 2017

DocID15274 Rev 10

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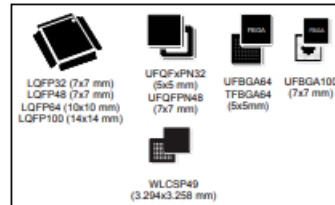
## STM32L072x8 STM32L072xB STM32L072xZ

Ultra-low-power 32-bit MCU Arm<sup>®</sup>-based Cortex<sup>®</sup>-M0+, up to 192KB Flash, 20KB SRAM, 6KB EEPROM, USB, ADC, DACs

Datasheet - production data

### Features

- Ultra-low-power platform
  - 1.65 V to 3.6 V power supply
  - -40 to 125 °C temperature range
  - 0.29 µA Standby mode (3 wakeup pins)
  - 0.43 µA Stop mode (16 wakeup lines)
  - 0.86 µA Stop mode + RTC + 20-Kbyte RAM retention
  - Down to 93 µA/MHz in Run mode
  - 5 µs wakeup time (from Flash memory)
  - 41 µA 12-bit ADC conversion at 10 ksps
- Core: Arm<sup>®</sup> 32-bit Cortex<sup>®</sup>-M0+ with MPU
  - From 32 kHz up to 32 MHz max.
  - 0.95 DMIPS/MHz
- Memories
  - Up to 192-Kbyte Flash memory with ECC(2 banks with read-while-write capability)
  - 20 -Kbyte RAM
  - 6 Kbytes of data EEPROM with ECC
  - 20-byte backup register
  - Sector protection against R/W operation
- Up to 84 fast I/Os (78 I/Os 5V tolerant)
- Reset and supply management
  - Ultra-safe, low-power BOR (brownout reset) with 5 selectable thresholds
  - Ultra-low-power POR/PDR
  - Programmable voltage detector (PVD)
- Clock sources
  - 1 to 25 MHz crystal oscillator
  - 32 kHz oscillator for RTC with calibration
  - High speed internal 16 MHz factory-trimmed RC (+/- 1%)
  - Internal low-power 37 kHz RC
  - Internal multispeed low-power 65 kHz to 4.2 MHz RC
  - Internal self calibration of 48 MHz RC for USB
  - PLL for CPU clock
- Pre-programmed bootloader
  - USB, USART supported
- Development support
  - Serial wire debug supported



- Rich Analog peripherals
  - 12-bit ADC 1.14 Msps up to 16 channels (down to 1.65 V)
  - 2 x 12-bit channel DACs with output buffers (down to 1.8 V)
  - 2x ultra-low-power comparators (window mode and wake up capability, down to 1.65 V)
- Up to 24 capacitive sensing channels supporting touchkey, linear and rotary touch sensors
- 7-channel DMA controller, supporting ADC, SPI, I2C, USART, DAC, Timers
- 11x peripheral communication interfaces
  - 1x USB 2.0 crystal-less, battery charging detection and LPM
  - 4x USART (2 with ISO 7816, IrDA), 1x UART (low power)
  - Up to 6x SPI 16 Mbps/s
  - 3x I2C (2 with SMBus/PMBus)
- 11x timers: 2x 16-bit with up to 4 channels, 2x 16-bit with up to 2 channels, 1x 16-bit ultra-low-power timer, 1x SysTick, 1x RTC, 2x 16-bit basic for DAC, and 2x watchdogs (independent/window)
- CRC calculation unit, 96-bit unique ID
- True RNG and firewall protection
- All packages are ECOPACK2

Table 1. Device summary

Reference	Part number
STM32L072x8	STM32L072V8
STM32L072xB	STM32L072V6, STM32L072R8, STM32L072CB, STM32L072NB
STM32L072xZ	STM32L072V2, STM32L072RZ, STM32L072CZ, STM32L072KZ

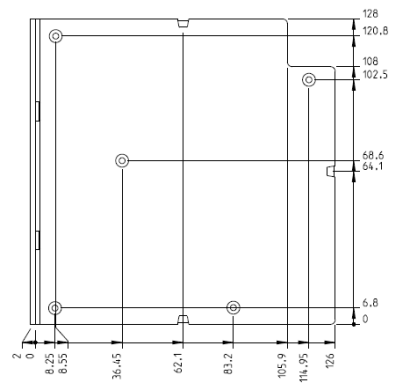
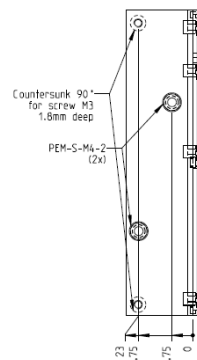
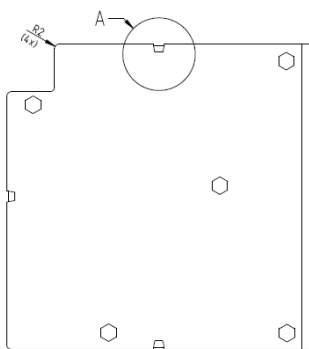
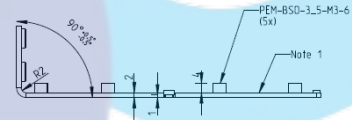
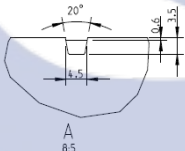
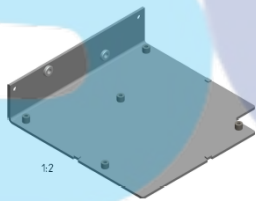
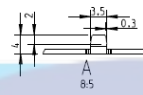
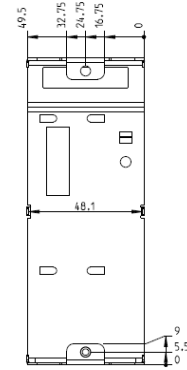
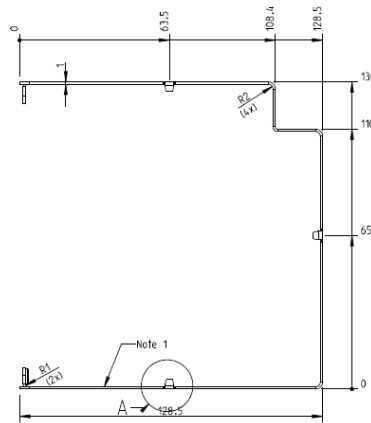
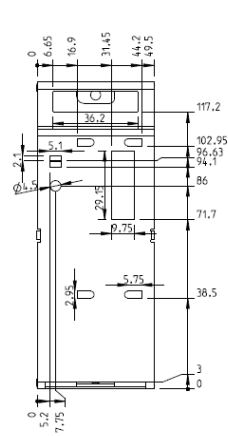
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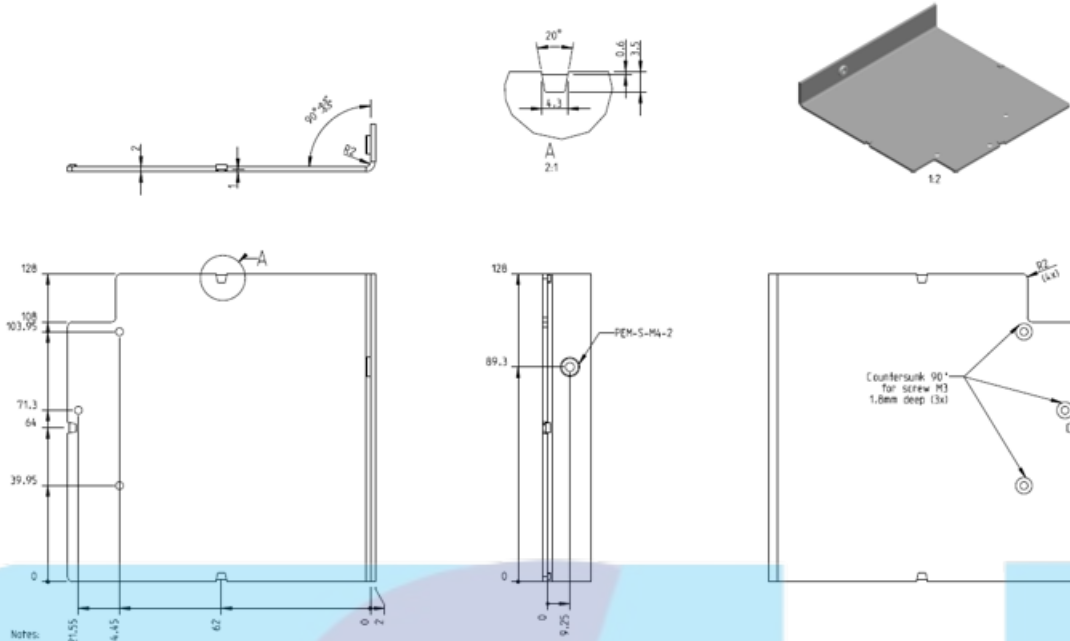
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Tes:



Technische Daten / Technical Specifications

DEUTSCH	ENGLISH	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
<b>Eingangsdaten</b>	<b>Input data</b>				
Minimum input voltage	DC input nominal Backstop band Emergency band	400 VDC 0 - 400 VDC 400 - 600 VDC	400 VDC 0 - 400 VDC 400 - 600 VDC	400 VDC 0 - 400 VDC 400 - 600 VDC	400 VDC 0 - 400 VDC 400 - 600 VDC
Maximum input voltage	DC input nominal Overvoltage error Overvoltage band Surge for 5 sec	750 VDC 1200 VDC 800 - 1200 VDC 800 - 880 VDC	750 VDC 1200 VDC 800 - 1200 VDC 800 - 880 VDC	750 VDC 1200 VDC 800 - 1200 VDC 800 - 880 VDC	750 VDC 1200 VDC 800 - 1200 VDC 800 - 880 VDC
Reverse polarity protection (L, L, Reverse polarity @ DC input nominal) Inrush current (25°F/W, @ULN, IN)	Reverse polarity protection (L, L, Reverse polarity @ DC input nominal) Inrush current (25°F/W, @ULN, IN)	180 A	180 A	180 A	180 A
Holding time (DC-LINK < 400VDC)	Holding time (DC-LINK < 400VDC)	>20 ms	>20 ms	>20 ms	>20 ms
Standby input power (up to 200VDC)	Standby input power (up to 200VDC)	7 W	7 W	7 W	7 W
Maximum own capacity (typ. @ 25°F/W, @ULN, IN)	Maximum own capacity (typ. @ 25°F/W, @ULN, IN)	100 μF	100 μF	100 μF	100 μF
Internal or external fuses (for max. inrush current 20 A's)	Internal or external fuses (for max. inrush current 20 A's)	5 A	5 A	5 A	5 A
<b>Ausgangsdaten DC-Netz</b>	<b>Output data DC-Net</b>				
Normal output voltage (DC+ Charge OUT)	Normal output voltage (DC+ Charge OUT)	400-800 VDC	400-800 VDC	400-800 VDC	400-800 VDC
DC+ Charge current (max.)	DC+ Charge current (max.)	5.4 A	5.4 A	5.4 A	5.4 A
Over current protection type (Charge Out)	Over current protection type (Charge Out)	Constant current	Constant current	Constant current	Constant current
Output power - Peak @ 75VDC	Output power - Peak @ 75VDC	300 W	300 W	750 W	2750 W
Output energy - Normal (during pre max. charge time 2s, @IN, 65VDC)	Output energy - Normal (during pre max. charge time 2s, @IN, 65VDC)	500 Wh	500 Wh	1300 Wh	4500 Wh
Output energy - Peak (during pre max. charge time 2s, @IN, 75VDC)	Output energy - Peak (during pre max. charge time 2s, @IN, 75VDC)	1200 Wh	1200 Wh	3000 Wh	10500 Wh
Maximum capacitive load to PC-DC-Diode (25°F/W, @ULN, IN) Pre-charge time	Maximum capacitive load to PC-DC-Diode (25°F/W, @ULN, IN) Pre-charge time	0.72 mF	0.72 mF	1.63 mF	5.13 mF
Minimum load during pre-charge (30A/W up to 10s @ULN, IN)	Minimum load during pre-charge (30A/W up to 10s @ULN, IN)	78 mA	78 mA	195 mA	573 mA
Output voltage (± 20%)	Output voltage (± 20%)	4.5 A	4.5 A	4.5 A	4.5 A
Output current (± 20%)	Output current (± 20%)	4.5 A	4.5 A	4.5 A	4.5 A
Maximum continuous output current (depending on used relays)	Maximum continuous output current (depending on used relays)	4.5 A	4.5 A	4.5 A	4.5 A
<b>Umweltbedingungen</b>	<b>Environmental conditions</b>				
Umgebungstemperatur, Betrieb / Lagerung (Transport)	Ambient temperature, operational / storage (transport)	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
Over temperature protection	Over temperature protection	-25...+70 °C (-10...+165 °F)	-25...+70 °C (-10...+165 °F)	-25...+70 °C (-10...+165 °F)	-25...+70 °C (-10...+165 °F)
Max. zul. Luftfeuchtigkeit im Betrieb (non-condensing, EC0006-10/1)	Max. permitted humidity during operation (non-condensing, EC0006-10/1)	5%...95% RH	5%...95% RH	5%...95% RH	5%...95% RH
<b>Spezifische Daten</b>	<b>General data</b>				
Vorgesehen für DC-Grid bis @60°C Umgebung	Vorgesehen für DC-Grid bis @60°C Umgebung	40.5 A	40.5 A	100.5 A	500.5 A
Schutzart	Degree of protection	IP20	IP20	IP20	IP20
Schutzklasse mit PE-Anschluss	Protection class with PE connection	II / III	II / III	II / III	II / III
Überspannungskategorie gemäß EN 61010-1 / UL 61010-1	Overvoltage category according to EN 61010-1 / UL 61010-1	II / III	II / III	II / III	II / III
Minimum surge current gemäß EN 61010-1 oder UL 61010-1	Position degree according to EN 61010-1 or UL 61010-1	2	2	2	2
Isolationsspannung Eingang-Ausgang / Eingang-Erde / Ausgang-Erde	Insulation voltage input-output / input-earth / output-earth	6 kV AC / 9 kV AC / 15 kV AC, 1 min.	6 kV AC / 9 kV AC / 15 kV AC, 1 min.	6 kV AC / 9 kV AC / 15 kV AC, 1 min.	6 kV AC / 9 kV AC / 15 kV AC, 1 min.
Metallgehäuse	Metal housing	beige/gray	beige/gray	beige/gray	beige/gray
Einbaulage (Mounting position)	Mounting position (Mounting position)	up to 5000 h	up to 5000 h	up to 5000 h	up to 5000 h
MTBF (EN 28000)	MTBF (EN 28000)	100,000 h (25 °C) / 100,000 h (40 °C) / 40,000 h (60 °C)	100,000 h (25 °C) / 100,000 h (40 °C) / 40,000 h (60 °C)	100,000 h (25 °C) / 100,000 h (40 °C) / 40,000 h (60 °C)	100,000 h (25 °C) / 100,000 h (40 °C) / 40,000 h (60 °C)
Größe x Breite x Tiefe	Height x Width x Depth	50 x 50 x 130 mm	50 x 50 x 130 mm	50 x 50 x 130 mm	50 x 50 x 130 mm
Gewicht	Weight	80 kg	80 kg	80 kg	80 kg
<b>Abmessungen</b>	<b>Dimension data</b>				
PUH N Power Connector	PUH N Power Connector	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
Anzahl Klammern	Number of terminals	4 (L+, PE, L-, PC L+)	4 (L+, PE, L-, PC L+)	4 (L+, PE, L-, PC L+)	4 (L+, PE, L-, PC L+)
Leiterschnitt fest	Fixed wire cross-section	0.5...16 mm²	0.5...16 mm²	0.5...16 mm²	0.5...16 mm²
Leiterschnitt flexibel	Flexible wire cross-section	0.5...25 mm²	0.5...25 mm²	0.5...25 mm²	0.5...25 mm²
Leiterschnitt AWG	Wire cross-section AWG	18...4	18...4	18...4	18...4
Abschleiflänge	Insulation stripping length	18 mm	18 mm	18 mm	18 mm
Schraubendrehlänge	Screwdriver blade	0.8 x 4.5 mm	0.8 x 4.5 mm	0.8 x 4.5 mm	0.8 x 4.5 mm
PUH N Signal Connector	PUH N Signal Connector	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
Anzahl Klammern	Number of terminals	6 (E+, GND, E-, E-, GND)	10 (E+, GND, E-, GND, E-, E-, GND, 3kV, GND)	10 (E+, GND, E-, GND, E-, E-, GND, 3kV, GND)	10 (E+, GND, E-, GND, E-, E-, GND, 3kV, GND)
Leiterschnitt fest	Fixed wire cross-section	0.2...1.5 mm²	0.2...1.5 mm²	0.2...1.5 mm²	0.2...1.5 mm²
Leiterschnitt flexibel	Flexible wire cross-section	0.2...1.5 mm²	0.2...1.5 mm²	0.2...1.5 mm²	0.2...1.5 mm²
Leiterschnitt AWG	Wire cross-section AWG	20...14	20...14	20...14	20...14
Abschleiflänge	Insulation stripping length	5 mm	5 mm	5 mm	5 mm
Schraubendrehlänge	Screwdriver blade	0.8 x 2.5 mm	0.8 x 2.5 mm	0.8 x 2.5 mm	0.8 x 2.5 mm
<b>EMV / Schutz / Vibration</b>	<b>EMC / Shield / Vibration</b>				
Störabstrahlung gemäß	Interference radiation acc. to	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
Störleistungsempfänger gemäß	Interference immunity tests acc. to	DIN EN IEC 10004-4	DIN EN IEC 10004-4	DIN EN IEC 10004-4	DIN EN IEC 10004-4
DC-Grid transient test @ 400VDC	DC-Grid transient test @ 400VDC	DIN EN IEC 11000-2	DIN EN IEC 11000-2	DIN EN IEC 11000-2	DIN EN IEC 11000-2
Freistufig gegen Schock gemäß IEC 60068-2-7	Shock resistance according to IEC 60068-2-7	System output DC, industry	System output DC, industry	System output DC, industry	System output DC, industry
Freistufig gegen Vibration gemäß IEC 60068-2-6	Shock resistance according to IEC 60068-2-6	30 g	30 g	30 g	30 g
Zusätzliche Richtlinien	Applied standards	4.0	4.0	4.0	4.0
Angeordnete Normen	Applied standards	MAX PCU DC650 40	TOP PCU DC650 40	TOP PCU DC650 100	TOP PCU DC650 500
Schutzanordnung gemäß	Protection safety voltage acc. to	ENIECUA 61010-1, ENIECUA 61010-2-201	ENIECUA 61010-1, ENIECUA 61010-2-201	ENIECUA 61010-1, ENIECUA 61010-2-201	ENIECUA 61010-1, ENIECUA 61010-2-201
Sichere Trennung / Schutz gegen elektrischen Schlag	Protective isolation / protection against electrical shock	SELV EN 61236-1, SELV EN 61236-1	SELV EN 61236-1, SELV EN 61236-1	SELV EN 61236-1, SELV EN 61236-1	SELV EN 61236-1, SELV EN 61236-1
Schutz gegen gefährliche Körperberührung	Body protection against dangerous electrical shock	VOE 6100-10 / DIN 57 100-410	VOE 6100-10 / DIN 57 100-410	VOE 6100-10 / DIN 57 100-410	VOE 6100-10 / DIN 57 100-410
Schutz gegen gefährliche Körperberührung	Body protection against dangerous electrical shock	VOE 6100-10	VOE 6100-10	VOE 6100-10	VOE 6100-10

- Bedienungsanleitung
- Operating instructions
- PC Charge Unit

Produktfamilie	202160000
MAX PCU DC650 40	202160000
TOP PCU DC650 40	202160000
TOP PCU DC650 100	202160000
TOP PCU DC650 500	202161000

Zubehör / Accessories / Accessoires / Accessoir / Accessoires / 8019	2000000000000000
MFA 30 MF	101100000
MFA 60 MF	101110000

Weldmüller

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Abbildung Bereich / Illustration area

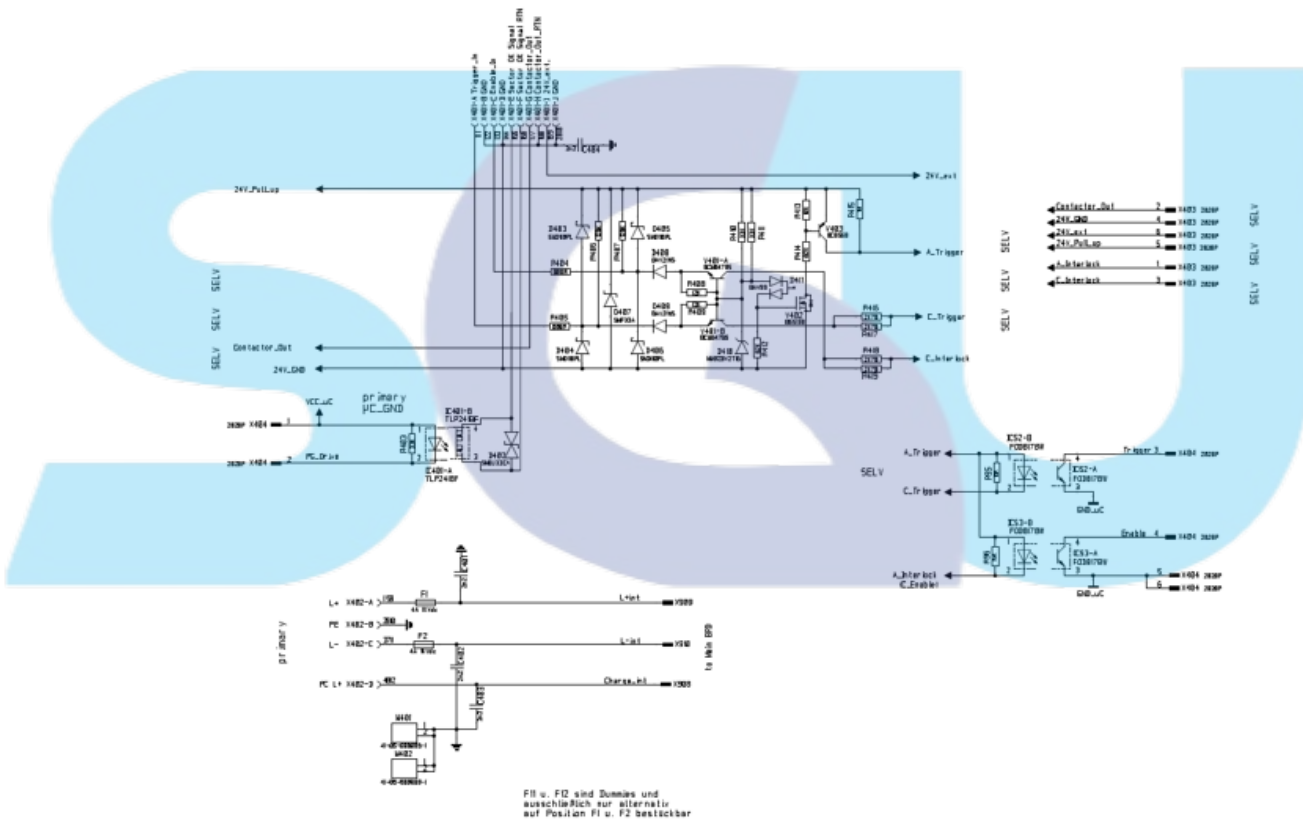
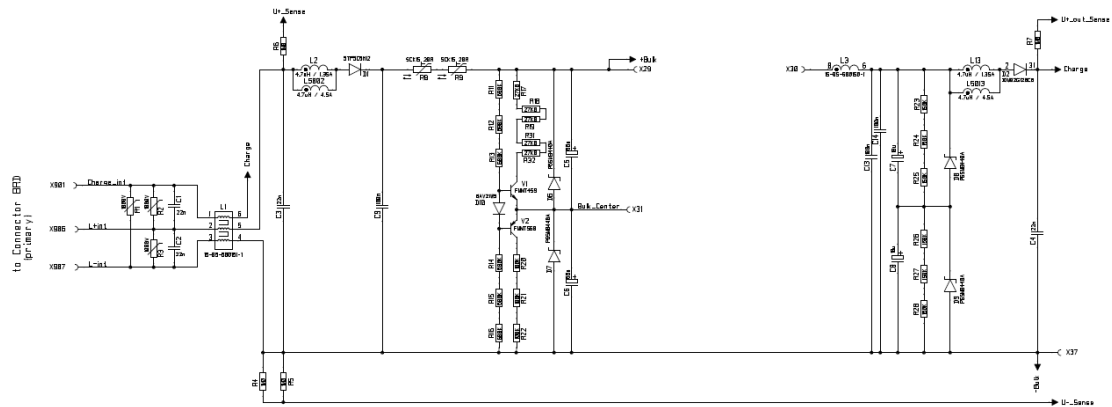


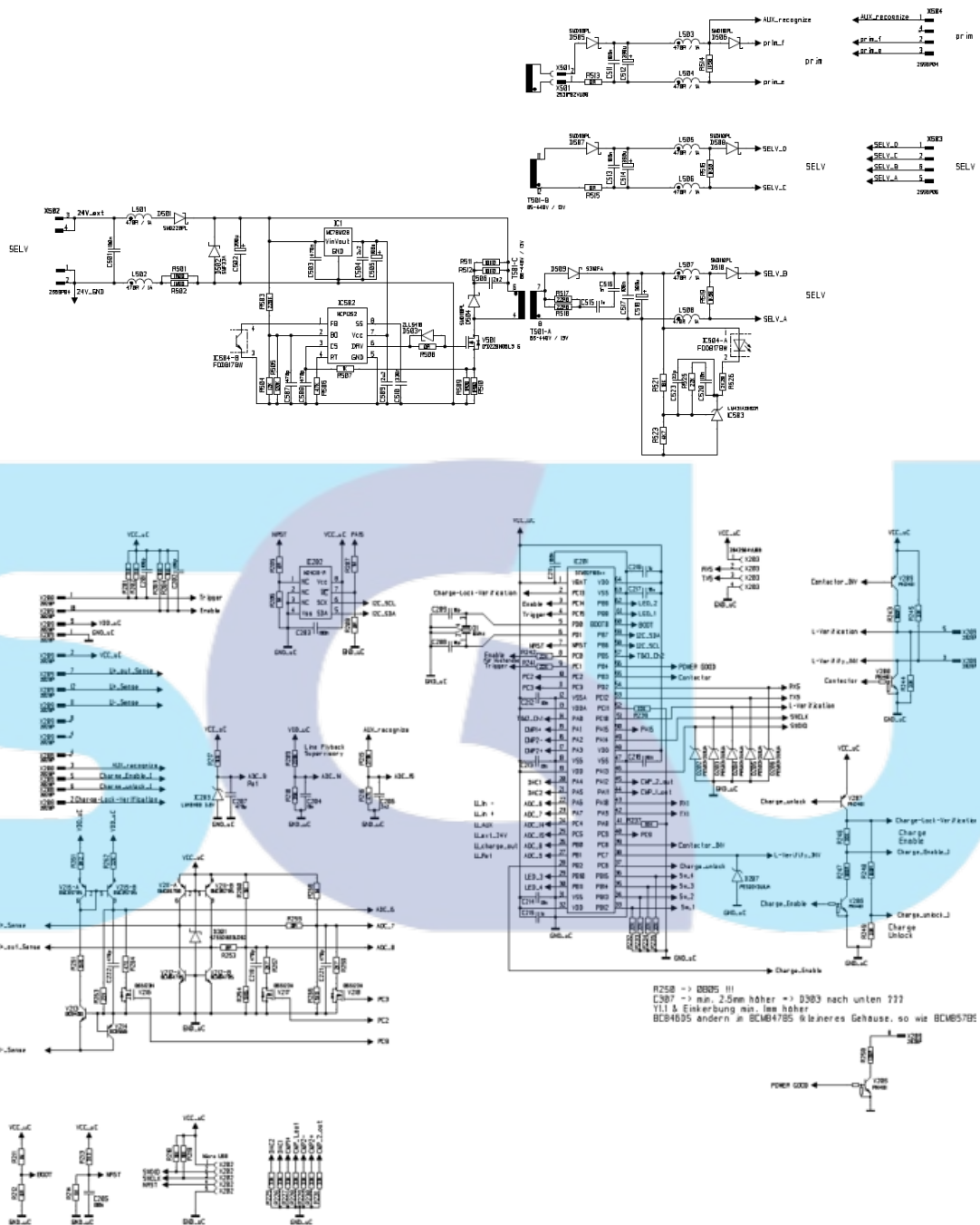
Sicherheits- und Warnhinweise

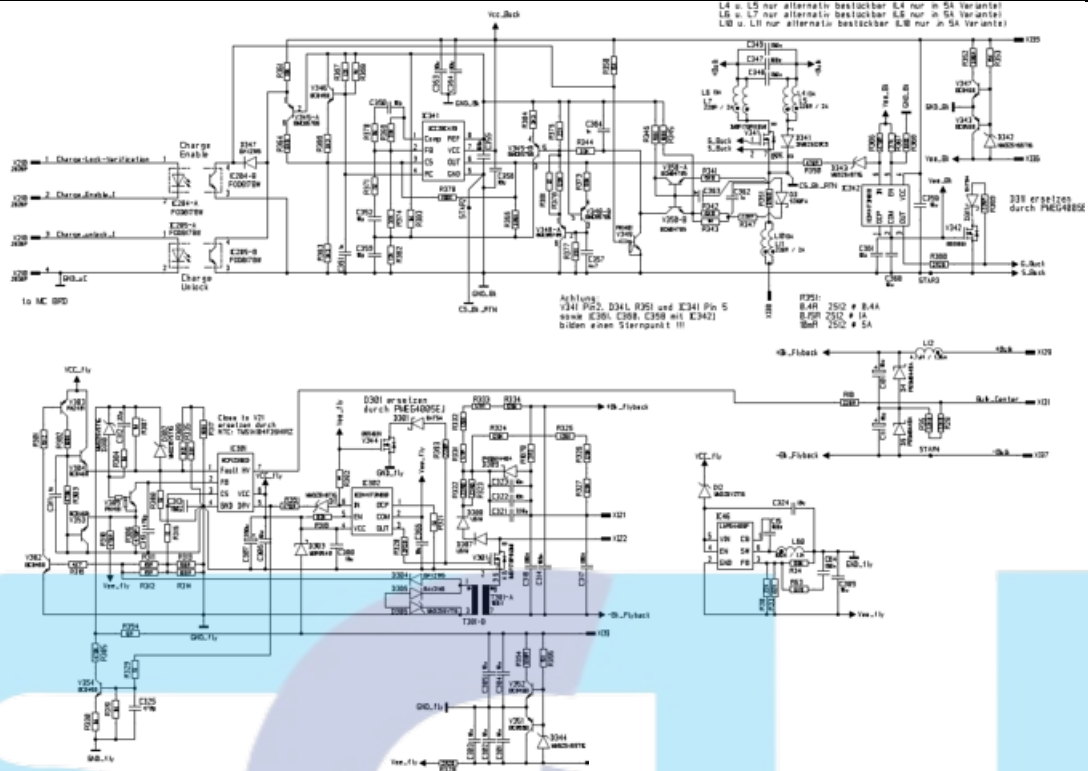
DIN EN IEC 10004-4  
DIN EN IEC 11000-2  
System output DC, industry  
30 g  
4.0

Safety Notice and Warnings

This device is only intended for use as described in the operating instructions. Any other type of usage is forbidden and can lead to accidents or destruction of the device.  
Using the device in non-approved applications will lead immediately to the expiration of all guarantee and warranty claims on the part of the operator against the manufacturer.







NO	TOP KCU DC/DC 40 Cover DC/DC	NO	TOP KCU DC/DC 40 Cover DC/DC
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99	99	99	99
100	100	100	100

DESIGN AND CONSTRUCTION OF NEW PRE-CHARGING UNIT FOR NEW PRODUCT FAMILIES OF AECONVERSION BELONGING TO DC-GRID APPLICATION

Table with 4 columns: Part Number, Description, Quantity, and Unit. The table lists various components like capacitors, diodes, resistors, and fuses with their respective quantities and units. A large blue watermark 'AECON' is overlaid on the table.



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## CURRICULUM VITAE

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### Personal Data

Name:	Kevin Gunawan
Place of birth:	Jakarta
Birth date:	09.04.2001
Adress:	Jalan Rawa Selatan 1 No. 33 Jakarta 10550, Indonesia
Nationality:	Indonesian
Marriage status:	Single
Telephone number:	+6221 4264436
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LinkedIn:	<a href="https://www.linkedin.com/in/kevin-gunawan-62b9341b3">linkedin.com/in/kevin-gunawan-62b9341b3</a>



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### Study

Since 2019	Mechatronics engineering 8th semester at Swiss German University
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### Internship

March to August 2022	Intern at Aeconversion GmbH & Co. KG, an industrial electronics company with a focus on the manufacture of inverters and power supplies. Activities: Participate in quality control, product testing, and logistics for photovoltaic inverters and power supplies.
June to July 2021	Intern at PT. Trimukti Wirapratama, an Indonesian shoe manufacturer, in the design, production and quality control departments. Activities: Participate in the development of shoe soles, the manufacture of shoe soles and the quality control of shoe soles.
Juli bis August 2018	Intern at PT. Gramaselindo Utama, an Indonesian fiber optic cable manufacturer, in production and quality control. Activities: Participate in fiber optic cable introduction, fiber optic cable manufacturing and quality control.

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### School education

2016 to 2019	Strada Vocational Highschool
2012 to 2016	Van Lith Middle Highschool
2006 to 2012	Saint John Elementary School

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### Course

2016-2018	German language course at Goethe-Institut Indonesia
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### Linguistic proficiency

Indonesian – Mother tongue  
English – Very good in word and scripture  
German – Level B1.3

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### Computer knowledge

Microsoft Windows	FluidSim	Scilab
Microsoft Office (Word, Excel, PowerPoint)	Arduino	Codesys
Fusion 360	Proteus 8	Qt
Minitab		

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### Organizational experience

2021-2022	Head of student organization with military discipline at the university
2019-2021	Secretary of the student organization with military discipline at the university
2021	Deputy Head of Events Department for "Mechatronics Day 2021"
2020	Member of the events department for "Mechatronics Day 2020"
2019-2020	Active member of the organizing club at the university
2019	Member of the logistics department for "Mechatronics Day 2019"
2017-2018	Chairman of the Student Council in the vocational school

## Hobbys and Interests

Swimming  
Riding motorbike  
Badminton

Cooking  
Travel  
Tracking

Electrical engineering  
Automation  
Quality control

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**Jakarta, 10. June 2023**



**Kevin Gunawan**

